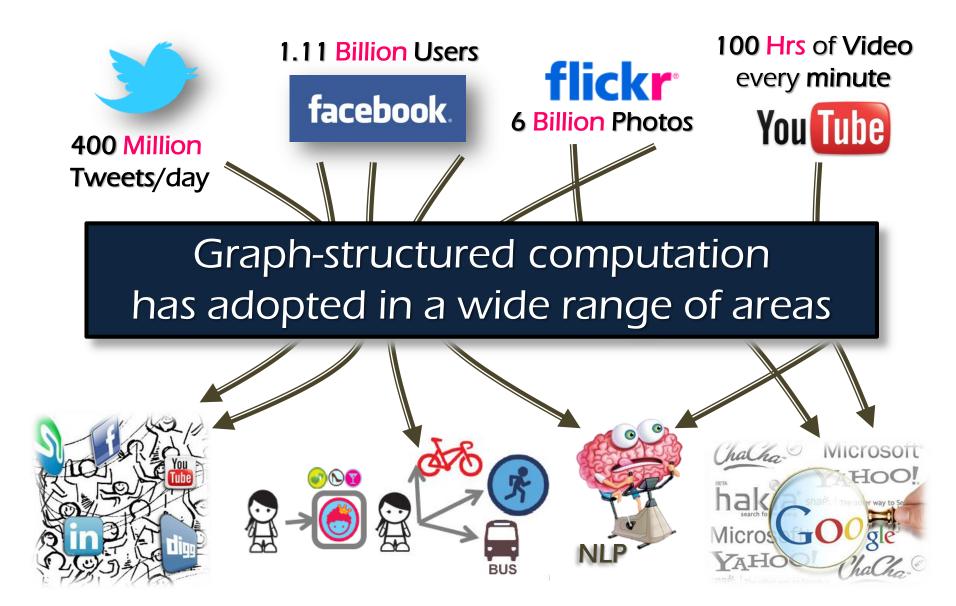
SYNC or **ASYNC**? Time to Fuse for Distributed Graph-Parallel Computation

<u>Chenning Xie</u>⁺, Rong Chen⁺, Haibing Guan^{*}, Binyu Zang⁺ and Haibo Chen⁺

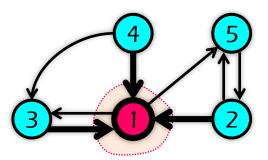
Institute of Parallel and Distributed Systems ⁺ Department of Computer Science ^{*} Shanghai Jiao Tong University

Big Data \rightarrow Graph Computation



Graph-parallel Computation

"<u>Think as Vertex</u>", e.g. **PageRank**: $R_i = \alpha + (1-\alpha)$ $W_{i,j} R_j$ $i,j \in E$



Characteristics

- □ Linked set \rightarrow data dependence
- \Box Rank of who links it \rightarrow predictable accesses
- \Box Convergence \rightarrow iterative computation

Distributed Graph Computation

Larger Graph
 Complicated Computation
 Storage support

Distributed Graph Computation

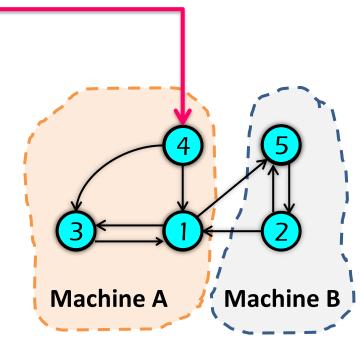
User define the logic (e.g. <u>Pagerank</u>) :

- □ Input: R_i (Data of neighbor j)
- Compute():

Vertex_Data = $\alpha + (1-\alpha) \sum N_j R_j$

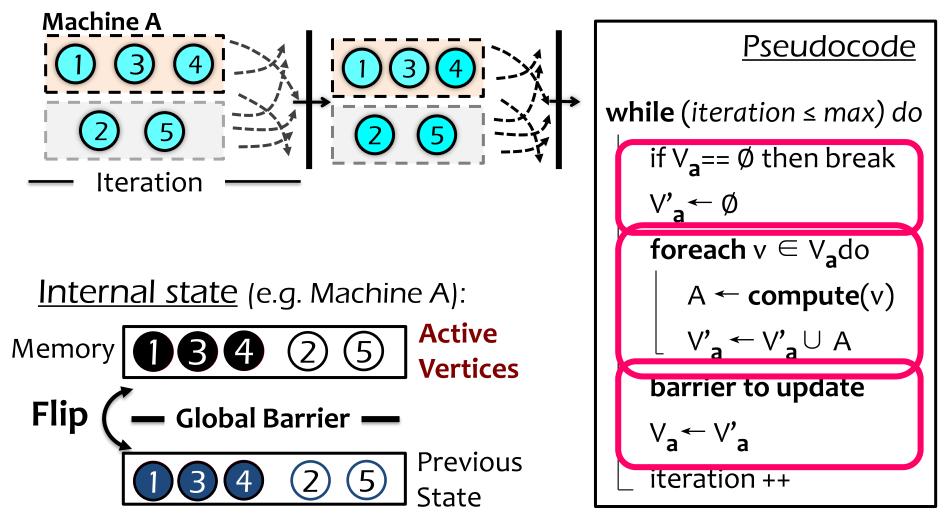
Framework:

- Load & partition over cluster
- Schedule compute() <u>Repeatedly</u> until get convergence



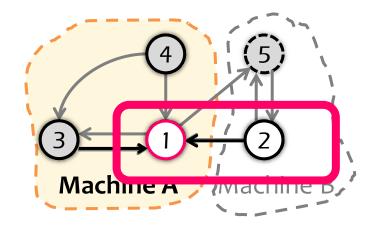
Existing Scheduling Modes - <u>Synchronous</u> (Sync Mode)

Scheduling:

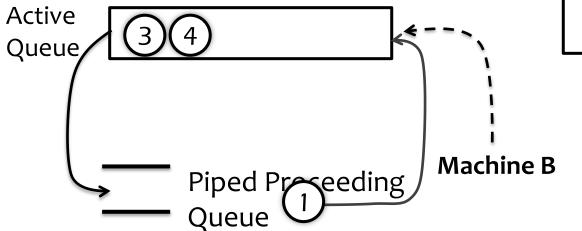


Existing Scheduling Modes - <u>Asynchronous</u> (Async Mode)

Scheduling:



Internal State (e.g. Machine A):



 $\frac{\text{Pseudocode}}{\text{while}(\forall_a != \emptyset) \text{ do}}$ $v = \text{dequeue}(\forall_a)$ $A \leftarrow \text{compute}(v)$ $\forall'_a \leftarrow \forall'_a \cup A$ signal across machines

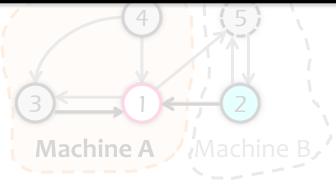
Propagate ASAP, to converge faster

Existing Scheduling Modes

Synchronous

Machine A

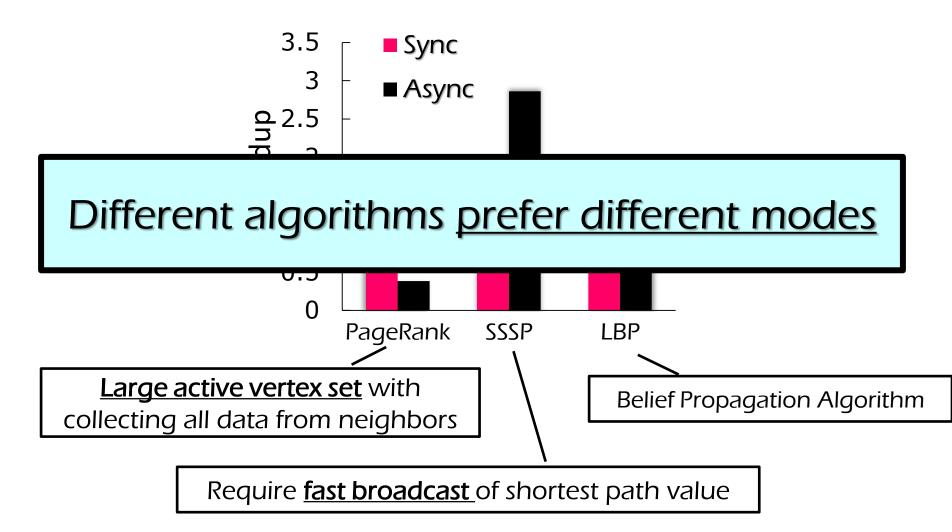
Which could get a **better performance**?



Asynchronous

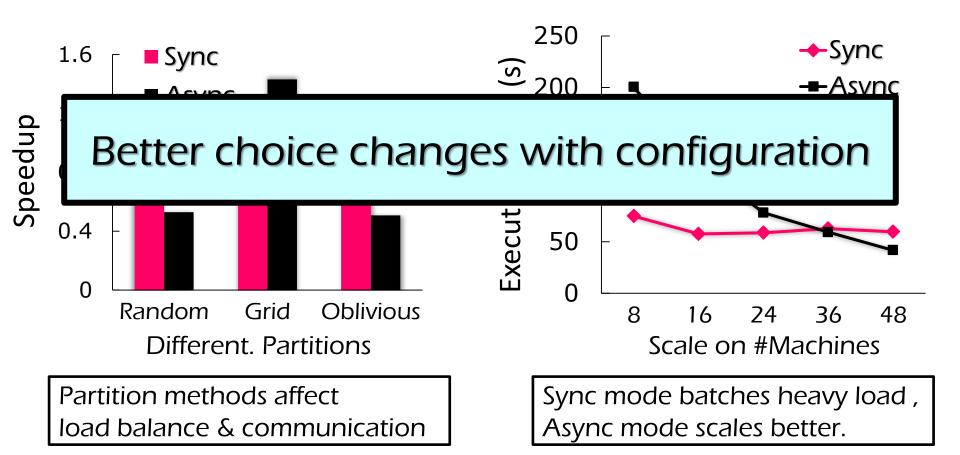
Algorithms: Sync vs. Async

□ Same Configuration + Different Algorithms?



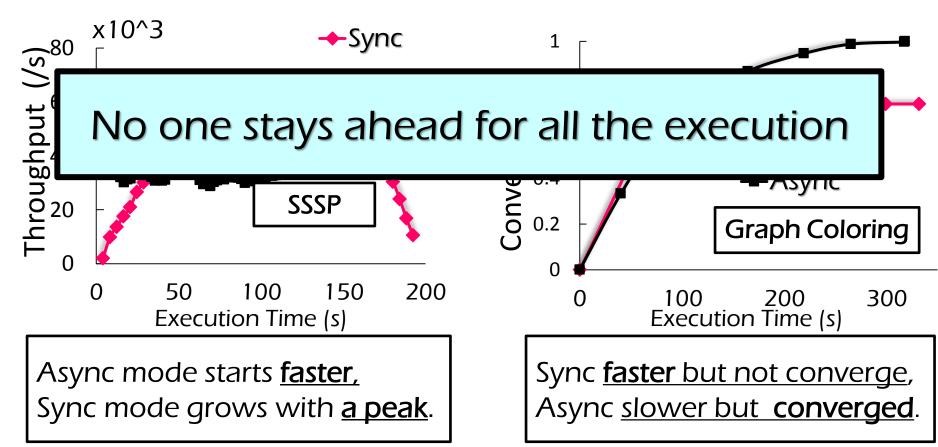
Configuration: Sync vs. Async

Same Configuration + Different Algorithms: Uncertain
 Different Configuration + Same Algorithms (LBP) ?



<u>Stages</u>: Sync vs. Async

- Same Configuration + Different Algorithms: Uncertain
- Different Configuration + Same Algorithms: Uncertain
- Same Configuration + Same Algorithm ?



Summery: Sync vs. Async

Properties	SYNC	VS.	ASYNC
→ Communication	Regular		Irregular
Convergence	Slow		Fast

Better choice is <u>Unintuitive</u>

Single mode alone may be still Suboptimal

→ Workload Heavyweight Lightweight
→ Scalability | Graph | | Machines |

Contributions

First comprehensive study on Sync & Async modes

PowerSwitch – adaptive, fast & seamless switches

- Hybrid Execution Mode (<u>Hsync</u> Mode):
 - Dynamically and <u>transparently</u> support the correct mode switches
- Switch Timing Model:
 - Determine the more efficient mode combined with online sampling, offline profiling and heuristics



How to Switch - the Hsync mode

- Internal state conversion
- □ Consistency & correctness

When to Switch – the timing model

- Performance Metrics
- □ Current mode prediction
- □ The other mode estimation

Implementation

Evaluation

Challenges of switches

> <u>Convert state</u> at <u>Consistent</u> switch points

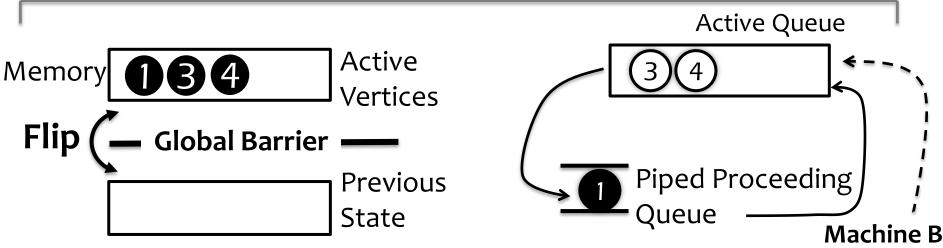
Sync mode

- Vertex update: <u>unordered</u>
- Flip in global barrier

Async mode

- <u>Priority/FIFO queue</u>
- Dequeue and enqueue

Internal state of one machine



Challenges of switches Hsync mode

Consistent switch points :

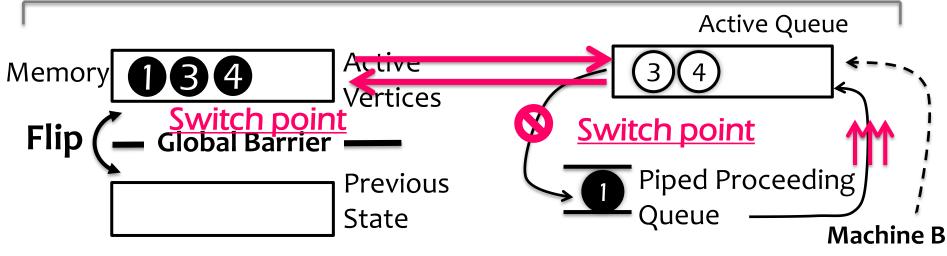
- Sync -> Async:
- □ Async -> Sync:

suspend & wait active vertex set

global barrier

Internal state of one machine

State transfer:





How to Switch - the Hsync mode

- Internal state conversion
- Consistency & correctness

When to Switch – the timing model

- Performance Metrics
- □ Current mode prediction
- □ The other mode estimation

Implementation

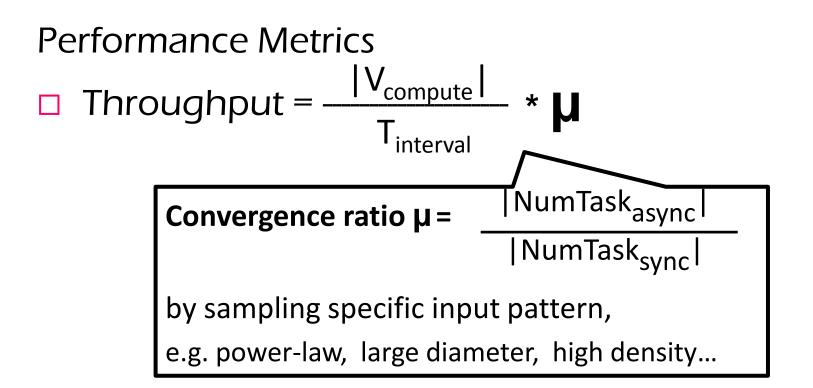
Evaluation

Switch timing - affected by lots of factors

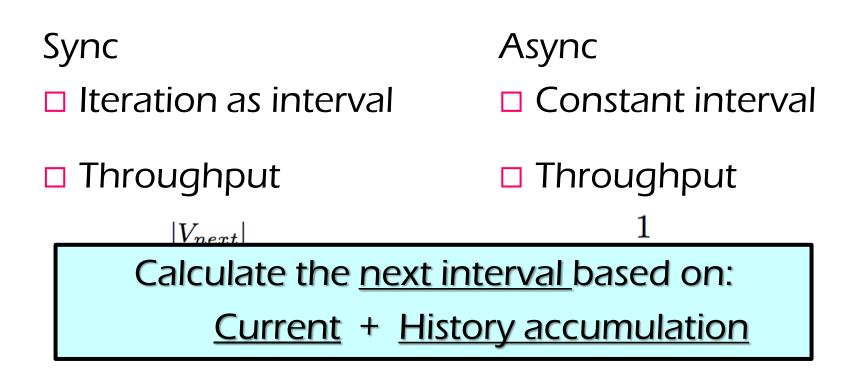
Challenges:

How to quantify the real-time performance?

How to obtain the metrics?



Predict Throughput for Current mode

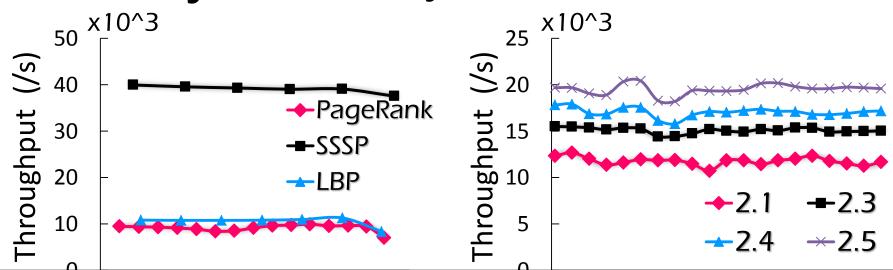


$$T_{vert_{next}} = \alpha \cdot T_{vert_{current}} + (1 - \alpha) \cdot T_{vert_{history}}$$

Predict for Other offline mode

No more execution information

Predict Async when in sync mode:



Solution

Online sampling: on subset of input in Async before start **Offline profiling**: build Neural Network model, refer to paper Predict **Sync** when in async mode:

□ Hard to predict exactly

□ Heuristic: Sync makes <u>high utilization</u> of resource.

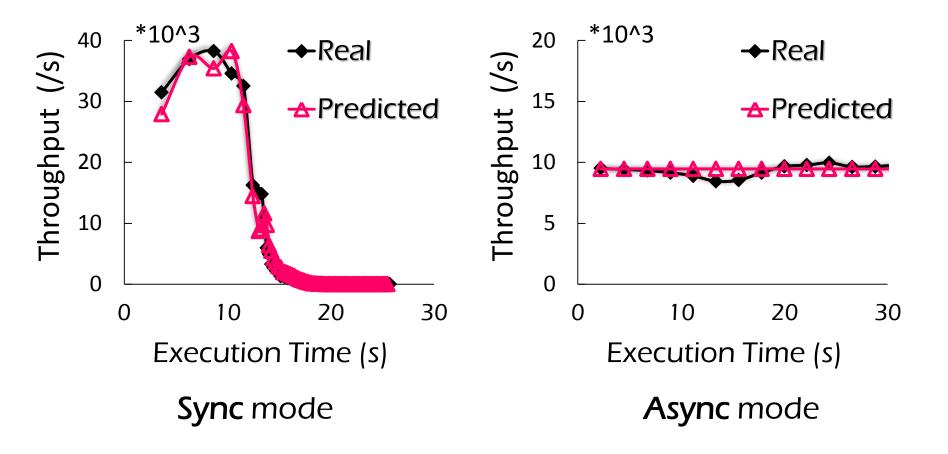
Thro_{Sync} > Thro_{Async}, if <u>workload is enough</u> Condition:

1. Number of active vertices increases
2. Workload :
$$\frac{|V_{new}|}{T}$$
 > Thro_{Async}

Async -> Sync

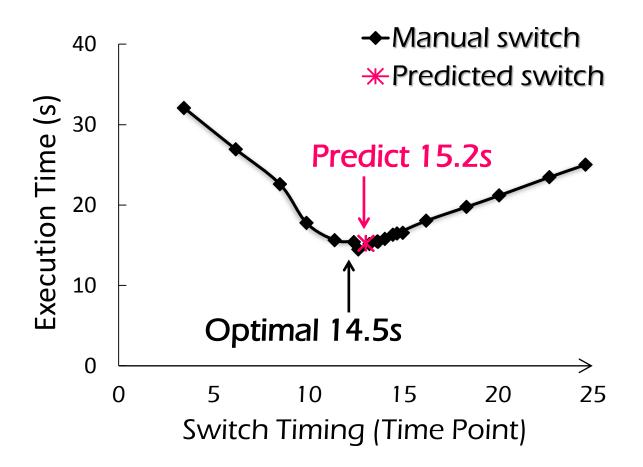
Prediction Accuracy

PageRank: Predicted throughput vs. Real sampled



Prediction Accuracy

PageRank: Predicted switch timing vs. Optimal



Implementation

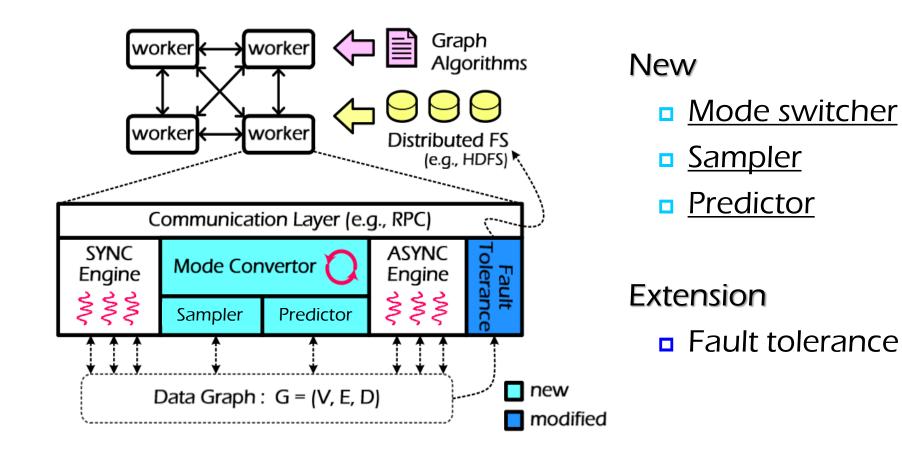
PowerSwitch:

- Based on latest GraphLab (PowerGraph) v2.2 with both Sync & Async modes.
- Provide the same graph abstraction transparent & compatible to all apps of GraphLab

Open Source

http://ipads.se.sjtu.edu.cn/projects/powerswitch.html

Implementation - Architecture



Evaluation

Baseline: original SYNC & ASYNC mode

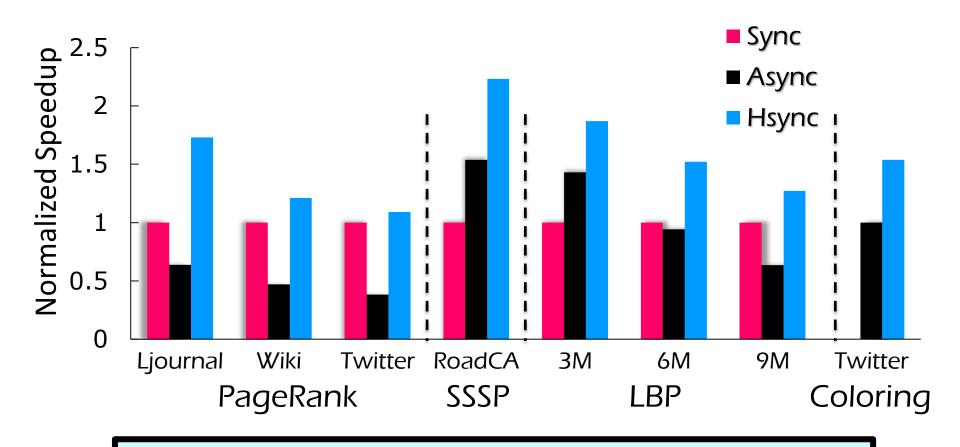
Configuration

- 48-node EC2-like cluster (VM based).
- Each node has 4 AMD Opteron cores, 12GB of RAM, connected with 1 GigE network.

Algorithms and Data Set

Algorithm	Graph	V	E
PageRank	Llournal	5.4M	79M
	Wiki	5.7M	130M
	Twitter	42M	1.47B
LBP	SYN-ImageData	1-12M	2-24M
SSSP	RoadCA	1.9M	5.5M
Coloring	Twitter	42M	1.47B

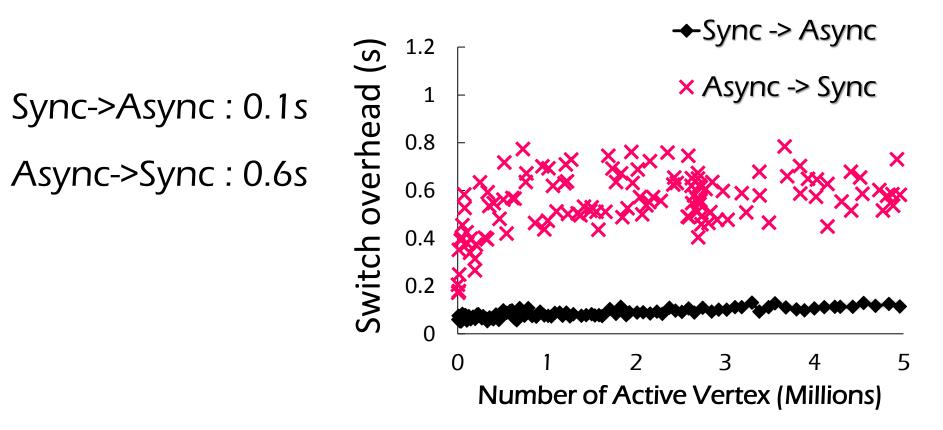
Performance Overview

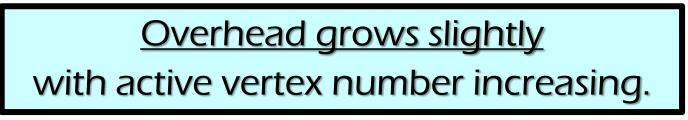


Outperform the baseline with best mode

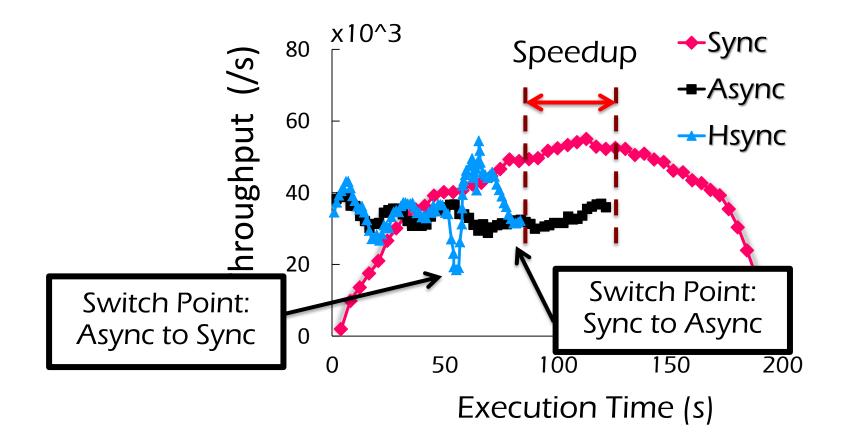
from 9% to 73% for all algorithms and dataset

Switch Overhead





Case: Single Source Shortest Path (SSSP)



Execution Mode: Async -> Sync-> Async



PowerSwitch

- A comprehensive analysis to the performance of Sync and Async modes for different algorithms, configuration and stages
- A Hsync mode that dynamically switch modes between Sync & Async to pursue optimal performance
- An effective switch timing model to predict suitable mode with sampling & profiling
- Outperforms GraphLab with best mode from 9% to 73% for various algorithms and dataset

Thanks

PowerSwitch

http://ipads.se.sjtu.edu.cn/ projects/powerswitch.html Institute of Parallel and Distributed Systems



Questions

